

# TEST PROGRAM SETS

Aim of the Test Program Set (TPS) is to make automatic verification test on specific electronic circuits such as analog, digital, power.



## Technical Data

<b>It can test the functions of</b>
Analog Circuits (RF, Filters etc.)
Digital Circuits (FPGA, CPLD, MCU, DSP, Flash etc.)
Power Electronic Circuit ( AC/AC converter, DC/DC converter, etc.)
<b>It can test the Digital Communication Interfaces</b>
I <sup>2</sup> C ,SPI, UART
RS485, RS422, RS232
MIL-STD-1553, ARINC429
<b>It can test using JTAG interface for boundary scan</b>
Boundary-scan test using ScanWorks tool



- Designing test program set for ATEs (Automatic Test Equipment's)
- Compatible with VPC products
- Designing developer and operator program using MATLAB and NI Teststand software
- Recording test reports

